



#### **General Description**

The AOZ6274 is a dual Double-Pole, Double-Throw (DPDT) analog switch that is designed to operate from a single 1.65V to 4.3V supply. The AOZ6274 features an ultra-low on resistance, excellent total harmonic distortion (THD) performance, and low power consumption. The device also features fast switching and guaranteed Break-Before-Make (BBM) switching, assuring the switches never shorts the driver.

#### **Features**

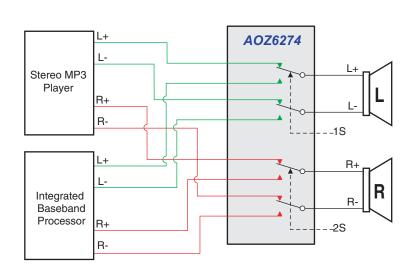
- Low On Resistance ( $R_{ON}$ ) for +2.7V supply (0.3 $\Omega$ )
- Low  $I_{CCT}$  current when nS input is lower than  $V_{CC}$
- $0.25\Omega$  maximum R<sub>ON</sub> flatness for +2.7V supply
- Small 3 x 3mm 16-Lead QFN Package
- Broad 1.65V to 4.30V V<sub>CC</sub> operating range
- Low THD (0.01% typical for  $32\Omega$  load)

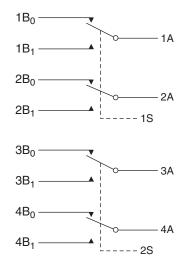
#### Applications

- Cell phone
- PDA
- Portable media player



## **Typical Application**





#### **Pin Configuration**



#### **Ordering Information**

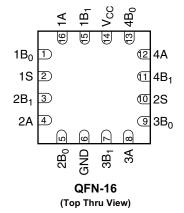
Part Number	Ambient Temperature Range	Package	Environmental		
AOZ6274QI	-40°C to +85°C	3x3 16-Lead QFN	Green		



AOS Green Products use reduced levels of Halogens, and are also RoHS compliant.

Please visit www.aosmd.com/web/quality/rohs\_compliant.jsp for additional information.

# Pin Configuration



# **Pin Description**

Pin Name	Function
1A, 2A, 3A, 4A, 1B <sub>0</sub> , 1B <sub>1</sub> , 2B <sub>0</sub> , 2B <sub>1</sub> , 3B <sub>0</sub> , 3B <sub>1</sub> , 4B <sub>0</sub> , 4B <sub>1</sub>	Data Ports
1S, 2S	Control Input

#### **Truth Table**

Logic Input	Function
0	nB <sub>0</sub> Connected to nA
1	nB <sub>1</sub> Connected to nA

#### **Absolute Maximum Ratings**

Exceeding the Absolute Maximum ratings may damage the device.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	-0.5V to +4.6V
V <sub>S</sub>	Switch Voltage	-0.5 to V <sub>CC</sub> + 0.3V
V <sub>IN</sub>	Input Voltage	-0.5V to +4.6V
I <sub>IK</sub>	Minimum Input Diode Current	-50mA
I <sub>SW</sub>	Switch Current	350mA
I <sub>SWPEAK</sub>	Peak Switch Current (Pulsed at 1ms duration, <10% Duty Cycle)	500mA
T <sub>STG</sub>	Storage Temperature Range	-65°C to +150°C
Т <sub>Ј</sub>	Maximum Junction Temperature	+150°C
ΤL	Lead Temperature (Soldering, 10 seconds)	+260°C
ESD	Human Body Model	6000V

#### **Recommend Operating Ratings**

The device is not guaranteed to operate beyond the Maximum Operating Ratings.

Symbol	Parameter	Rating
V <sub>CC</sub>	Supply Voltage	1.65V to 4.3V
V <sub>IN</sub>	Control Input Voltage <sup>(1)</sup>	0V to V <sub>CC</sub>
V <sub>SW</sub>	Switch Input Voltage	0V to V <sub>CC</sub>
T <sub>A</sub>	Operating Temperature	-40°C to +85°C

Note:

1. Unused inputs must be held HIGH or LOW. They may not float.



# **DC Electrical Characteristics**

Unless otherwise indicated, specifications indicate a temperature range of -40°C to +85°C. All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Units	
V <sub>IH</sub>	Input Voltage HIGH		4.3	1.4			V	
			2.7 to 3.6	1.3			1	
			2.3 to 2.7	1.1			1	
			1.65 to 1.95	0.9				
V <sub>IL</sub>	Input Voltage LOW		4.3			0.7	V	
			2.7 to 3.6			0.5		
			2.3 to 2.7			0.4		
			1.65 to 1.95			0.4		
I <sub>IN</sub>	Control Input Leakage	$V_{IN} = 0V$ to $V_{CC}$	1.65 to 4.30	-0.5		0.5	μA	
I <sub>NO(OFF)</sub> , I <sub>NC(OFF)</sub>	Off-Leakage Current of Port nB <sub>0</sub> and nB <sub>1</sub>	nA = 0.3V, V <sub>CC</sub> –0.3V, nB <sub>0</sub> or nB <sub>1</sub> = 0.3V, V <sub>CC</sub> –0.3V or floating	1.95 to 4.30	-50		50	nA	
I <sub>A(ON)</sub>	On Leakage Current of Port A	nA = 0.3V, $V_{CC}$ =0.3V, nB <sub>0</sub> or nB <sub>1</sub> = 0.3V, $V_{CC}$ =0.3V or floating	1.95 to 4.30	-60		60	nA	
R <sub>ON</sub>	Switch On Resistance <sup>(2)</sup>	I <sub>OUT</sub> = 100mA, nB <sub>0</sub> or nB <sub>1</sub> = 0V, 0.7V, 2.3V, 4.3V	4.3		0.25	0.4	Ω	
		I <sub>OUT</sub> = 100mA, nB <sub>0</sub> or nB <sub>1</sub> = 0V, 0.7V, 2.3V, 3.0V	3.0		0.27	0.4		
		I <sub>OUT</sub> = 100mA, nB <sub>0</sub> or nB <sub>1</sub> = 0V, 0.7V, 2.0V, 2.7V	2.7		0.3	0.4		
		I <sub>OUT</sub> = 100mA, nB <sub>0</sub> or nB <sub>1</sub> = 0V, 0.7V, 1.6V, 2.3V	2.3		0.4	0.7		
		I <sub>OUT</sub> = 100mA, nB <sub>0</sub> or nB <sub>1</sub> = 0V, 1.0V, 1.8V	1.8		0.8	1.8	1	
$\Delta R_{ON}$	On Resistance Matching	$I_{OUT} = 100$ mA, nB <sub>0</sub> or nB <sub>1</sub> = 0.7V	4.3		0.03	0.1	Ω	
	Between Channels <sup>(3)</sup>		3.0		0.03	0.1		
			2.7		0.03	0.1		
			2.3		0.03	0.1		
R <sub>FLAT(ON)</sub>	On Resistance Flatness <sup>(4)</sup>	$I_{OUT}$ = 100mA, B <sub>0</sub> or nB <sub>1</sub> = 0V to V <sub>CC</sub>	4.3		0.07	0.2	Ω	
			3.0		0.07	0.2		
			2.7		0.09	0.25		
			2.3		0.16	0.3		
I <sub>CC</sub>	Quiescent Supply Current	$V_{IN} = 0V$ to $V_{CC}$ , $I_{OUT} = 0A$	4.3	-500		500	nA	
I <sub>CCT</sub>	Increase in I <sub>CC</sub> per Input Con-	V <sub>IN</sub> = 1.8V	4.3		26.0	32.0	μA	
	trol Voltage	V <sub>IN</sub> = 2.6V			9.0	12.0		

Notes:

2. On resistance is determined by the voltage drop between A and B pins at the indicated current through the switch.

3.  $\Delta R_{ON}$  =  $R_{ONmax}$  –  $R_{ONmin}$  measured at identical  $V_{CC},$  temperature, and voltage.

4. Flatness is defined as the difference between the maximum and minimum value of R<sub>ON</sub> over the specified range of conditions.



#### **AC Electrical Characteristics**

Unless otherwise indicated, specifications indicate a temperature range of -40°C to +85°C. All typical values are at 25°C unless otherwise specified.

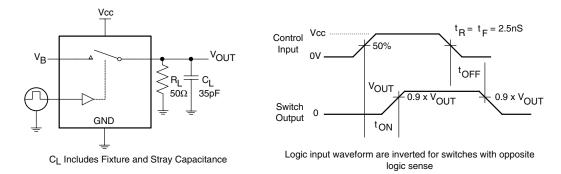
Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Units
t <sub>ON</sub>	Turn-On Time	$nB_0 \text{ or } nB_1 = 1.5V, R_L = 50\Omega, C_L = 35pF$	3.6 to 4.3		35	60	ns
			2.7 to 3.6		50	75	
			2.3 to 2.7		75	90	
t <sub>OFF</sub>	Turn-Off Time	$nB_0 \text{ or } nB_1 = 1.5V, R_L = 50\Omega, C_L = 35pF$	3.6 to 4.3		25	40	ns
			2.7 to 3.6		30	50	
			2.3 to 2.7		40	60	
t <sub>BBM</sub>	Break-Before-Make Time	$nB_0 \text{ or } nB_1 = 1.5V, R_L = 50\Omega, C_L = 35pF$	3.6 to 4.3		20		ns
			2.7 to 3.6		30		
			2.3 to 2.7		40		
Q	Charge Injection	$C_L = 100 pF, V_{GEN} = 0V, R_{GEN} = 0\Omega$	3.6 to 4.3		22		рС
			2.7 to 3.6		15		
			2.3 to 2.7		10		
OIRR	Off Isolation	f = 100kHz, $R_L = 50\Omega$ , $C_L = 5pF$	3.6 to 4.3		-70		dB
			2.7 to 3.6		-70		
			2.3 to 2.7		-70		
Xtalk	Crosstalk	f = 100kHz, $R_L = 50\Omega$ , $C_L = 5pF$	3.6 to 4.3		-70		dB
			2.7 to 3.6		-70		
			2.3 to 2.7		-70		
BW	-3dB Bandwidth	$R_L = 50\Omega$	2.3 to 4.3		>55		MHz
THD	Total Harmonic	$R_L = 32\Omega$ , $V_{IN} = 2V_{pp}$ , f = 20Hz to 20kHz	3.6 to 4.3		0.01		%
	Distortion		2.7 to 3.6		0.01		
			2.3 to 2.7		0.01		

#### Capacitance

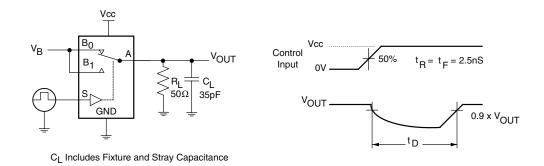
Unless otherwise indicated, specifications indicate a temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C. All typical values are at 25°C unless otherwise specified.

Symbol	Parameter	Conditions	V <sub>CC</sub> (V)	Min.	Тур.	Max.	Units
C <sub>IN</sub>	Control Pin Input Capacitance	f = 1MHz	0.0		2.0		pF
C <sub>OFF</sub>	B Port Off Capacitance	f = 1MHz	3.3		16		pF
C <sub>ON</sub>	A Port On Capacitance	f = 1MHz	3.3		116		pF

## **AC Loading and Waveforms**









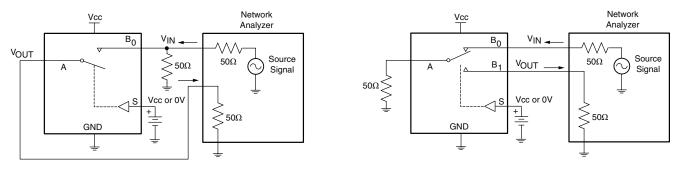
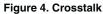
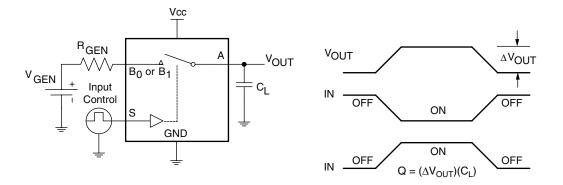


Figure 3. Off Isolation

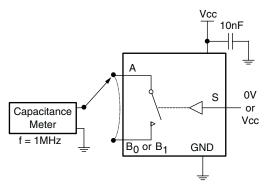


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## AC Loading and Waveforms (continued)







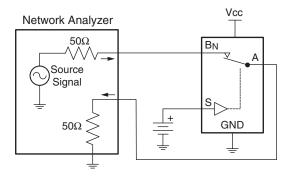
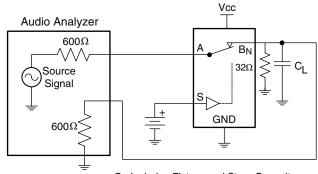


Figure 6. ON/Off Capacitance Measurement

Figure 7. Bandwidth



 $\rm C_L$  Includes Fixture and Stray Capacitance

Figure 8. Harmonic Distortion



Max.

0.032

0.002

0.012

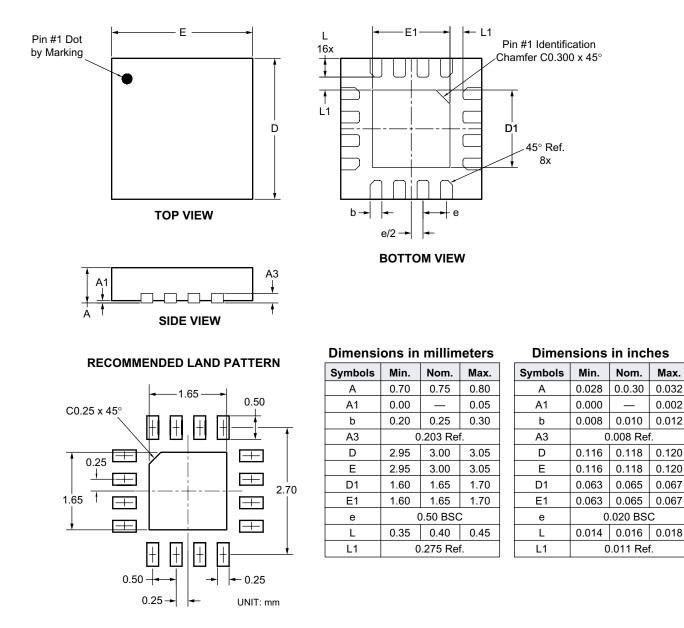
0.120

0.120

0.067

0.067

## Package Dimensions, QFN 3 x 3

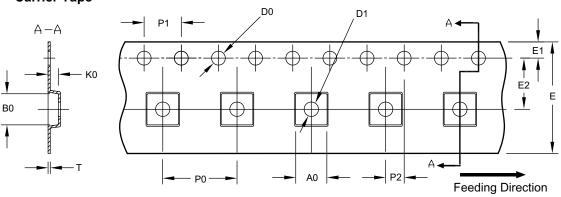


#### Note:

1. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

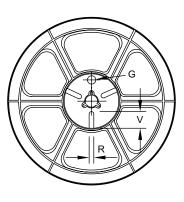
# Tape and Reel Dimensions, QFN 3 x 3

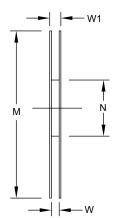


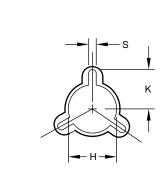


UNIT: mm												
Package	A0	B0	K0	D0	D1	Е	E1	E2	P0	P1	P2	т
DFN 3x3 EP	3.40 ±0.10	3.35 ±0.10	1.10 ±0.10	1.50 +0.10/-0	1.50 +0.10/-0	12.00 +0.30	1.75 ±0.10	5.50 ±0.05	8.00 ±0.10	4.00 ±0.10	2.00 ±0.05	0.30 ±0.05

Reel



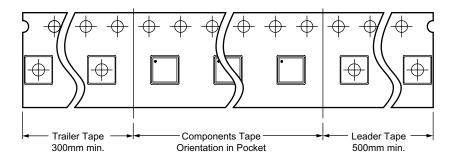




UNIT: mm

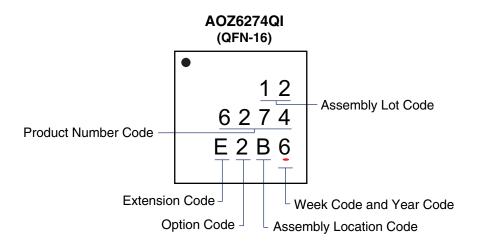
Tape Size	Reel Size	М	N	W	W1	н	к	S	G	R	v
12mm	ø330	ø330.0	ø97.00	13.00	17.40	ø13.0	10.60	2.00	_		_
		±0.50	±0.10	±0.30	±1.00	+0.50/-0.20		±0.50			

#### Leader/Trailer and Orientation





#### Part Marking



This datasheet contains preliminary data; supplementary data may be published at a later date. Alpha & Omega Semiconductor reserves the right to make changes at any time without notice.

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