

PC851XJ0000F Series

DIP 4pin High Collector-emitter Voltage Photocoupler



■ Description

PC851XJ0000F Series contains an IRED optically coupled to a phototransistor.

It is packaged in a 4-pin DIP, available in SMT gullwing lead-form option.

Input-output isolation voltage (rms) is $5.0 \, kV$.

Collector-emitter voltage is 350V.

■ Features

- 1. 4pin DIP package
- 2. Double transfer mold package (Ideal for Flow Soldering)
- 3. High collector-emitter voltage (V_{CEO}: 350V)
- 4. High isolation voltage between input and output (V_{iso(rms)}: 5.0 kV)
- 5. Lead-free and RoHS directive compliant

■ Agency approvals/Compliance

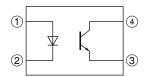
- 1. Recognized by UL1577, file No. E64380 (as model No. **PC851**)
- 2. Package resin: UL flammability grade (94V-0)

■ Applications

- 1. Telephone line interface/isolation
- 2. Interface to power supply circuit
- 3. Controller for SSRs, DC moters



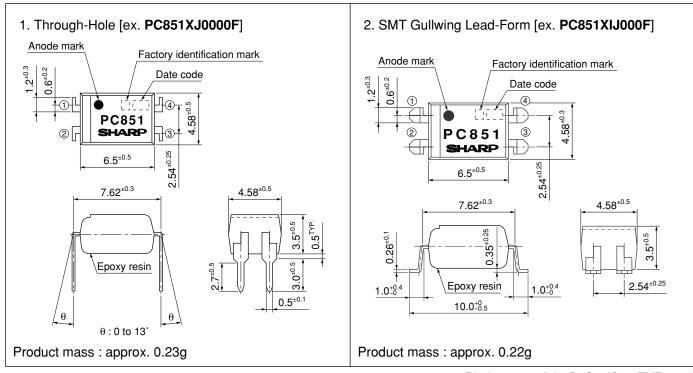
■ Internal Connection Diagram



- 1 Anode
- ② Cathode
- 3 Emitter
- 4 Collector

■ Outline Dimensions

(Unit: mm)



Plating material: SnCu (Cu: TYP. 2%)



Date code (2 digit)

1st digit				2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin		
no mark	Japan		
	Indonesia		
_	China		

^{*} This factory making is for identification purpose only.

Please contact the local SHARP sales representative to see the actual status of the production.

Rank mark

There is no rank mark indicator.



■ Model Line-up

Lead Form	Through Hole	SMT G	ullwing
Doolsogo	Sle	Taping	
Package	100pcs	2 000pcs/reel	
Model No.	PC851XJ0000F	PC851XIJ000F	PC851XPJ000F

Please contact a local SHARP sales representative to inquire about production status.



■ Absolute Maximum Ratings

■ Absolute Maximum Ratings (T _a =25°						
	Parameter	Symbol	Rating	Unit		
	Forward current	I_{F}	50	mA		
Input	*1 Peak forward current	I_{FM}	1	A		
Inj	Reverse voltage	V_R	6	V		
	Power dissipation	P	70	mW		
	Collector-emitter voltage	V_{CEO}	350	V		
Output	Emitter-collector voltage	V_{ECO}	6	V		
Out	Collector current	I_C	50	mA		
	Collector power dissipation	P_{C}	150	mW		
	Γotal power dissipation	P_{tot}	200	mW		
*2 Isolation voltage		V _{iso (rms)}	5.0	kV		
Operating temperature		T_{opr}	-25 to +100	°C		
Storage temperature		T _{stg}	-55 to +125	°C		
*3 Soldering temperature		T_{sol}	260	°C		

^{*1} Pulse width≤100µs, Duty ratio : 0.001 *2 40 to 60%RH, AC for 1 minute, f=60Hz *3 For 10s

■ Electro-optical Characteristics

 $(T_a=25^{\circ}C)$

	Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage		V_F	$I_F=20mA$	-	1.2	1.4	V
Input	Reverse Current		I_R	$V_R=4V$	-	-	10	μΑ
	Terminal capacitance		C_{t}	V=0, f=1kHz	-	30	250	pF
	Collector dark	llector dark current I _{CE}		$V_{CE}=200V, I_{F}=0$	-	-	1	μΑ
Output	Collector-emitter breakdown voltage		$BV_{CEO} \\$	$I_{C}=0.1 \text{mA}, I_{F}=0$	350	-	-	V
	Emitter-collector breakdown voltage		$\mathrm{BV}_{\mathrm{ECO}}$	$I_{E}=10\mu A, I_{F}=0$	6	-	-	V
	Collector current		I_{C}	$I_F=5mA$, $V_{CE}=5V$	2.0	4.0	_	mA
	Collector-emitter saturation voltage		V _{CE (sat)}	$I_F=20mA$, $I_C=1mA$	_	0.1	0.3	V
Transfer	Isolation resistance		$R_{\rm ISO}$	DC500V, 40 to 60%RH	5×10 ¹⁰	1×10 ¹¹	-	Ω
charac- teristics	Floating capacitance		C_{f}	V=0, f=1MHz	-	0.6	1.0	pF
	Cut-off frequency		f_C	V_{CE} =5 V , I_{C} =2 mA , R_{L} =100 Ω , -3 dB	_	50	_	kHz
	Daspansa tima	Rise time	t_r	V 2V I 2 A D 1000	_	4	10	μs
	Response time	Fall time		V_{CE} =2V, I_{C} =2mA, R_{L} =100 Ω	_	5	12	μs



Fig.1 Forward Current vs. Ambient Temperature

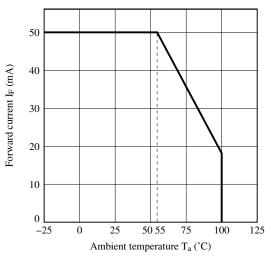


Fig.3 Collector Power Dissipation vs. Ambient Temperature

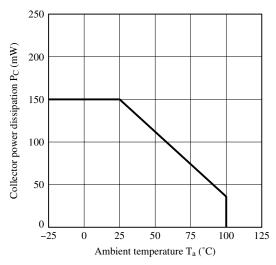


Fig.5 Peak Forward Current vs. Duty Ratio

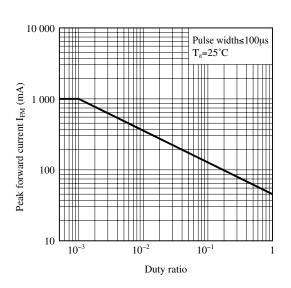


Fig.2 Diode Power Dissipation vs. Ambient Temperature

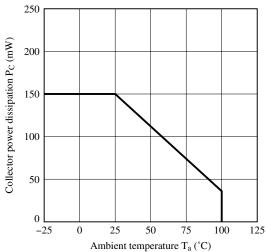


Fig.4 Total Power Dissipation vs. Ambient Temperature

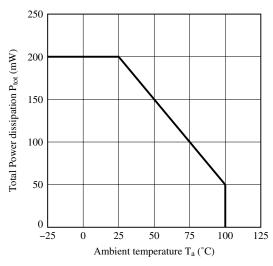


Fig.6 Forward Current vs. Forward Voltage

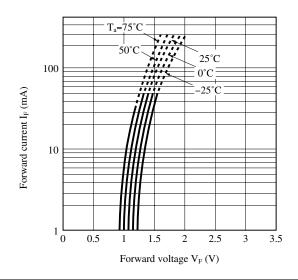




Fig.7 Current Transfer Ratio vs. Forward Current

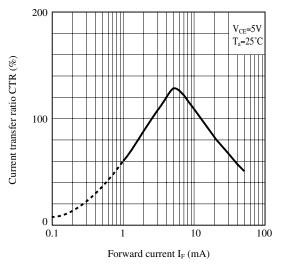


Fig.9 Relative Current Transfer Ratio vs.
Ambient Temperature

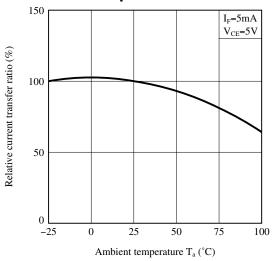


Fig.11 Collector Dark Current vs. Ambient Temperature

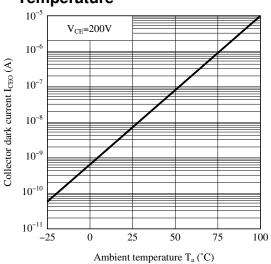


Fig.8 Collector Current vs. Collector-emitter Voltage

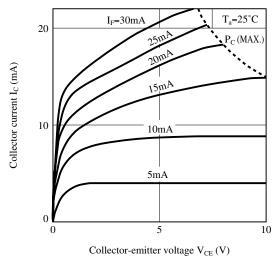


Fig.10 Collector - emitter Saturation Voltage vs. Ambient Temperature

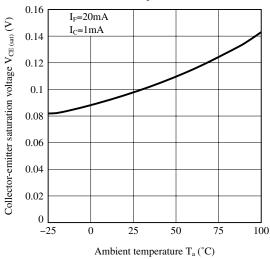
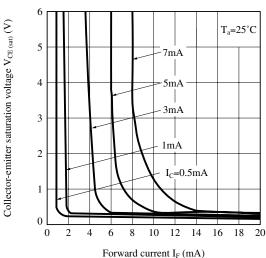


Fig.12 Collector-emitter Saturation Voltage vs. Forward Current



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Fig.13 Response Time vs. Load Resistance

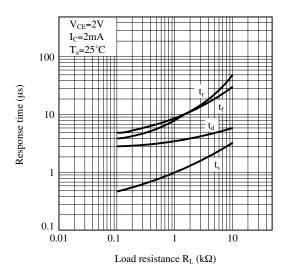
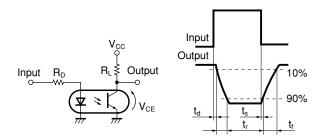


Fig.14 Test Circuit for Response Time



Please refer to the conditions in Fig.13.

Fig.15 Frequency Response

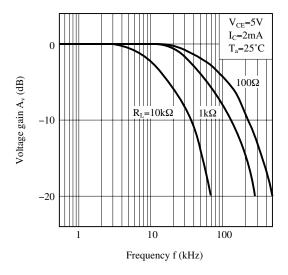
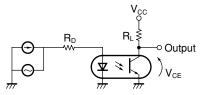


Fig.16 Test Circuit for Frequency Response



Please refer to the conditions in Fig.15.

Remarks: Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Design guide

While operating at I_F<1.0mA, CTR variation may increase.

Please make design considering this fact.

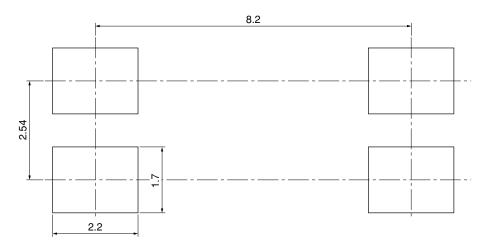
This product is not designed against irradiation and incorporates non-coherent IRED.

Degradation

In general, the emission of the IRED used in photocouplers will degrade over time.

In the case of long term operation, please take the general IRED degradation (50% degradation over 5 years) into the design consideration.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

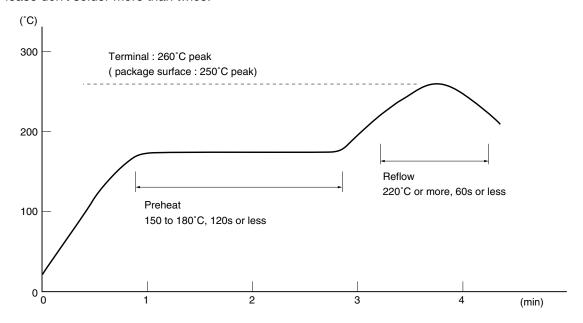
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 270°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3 minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this product.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

This product shall not contain the following materials banned in the RoHS Directive (2002/95/EC).

•Lead, Mercury, Cadmium, Hexavalent chromium, Polybrominated biphenyls (PBB), Polybrominated diphenyl ethers (PBDE).

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■ Package specification

Sleeve package

Package materials

Sleeve: HIPS (with anti-static material)

Stopper: Styrene-Elastomer

Package method

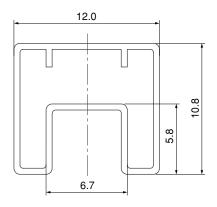
MAX. 100pcs of products shall be packaged in a sleeve.

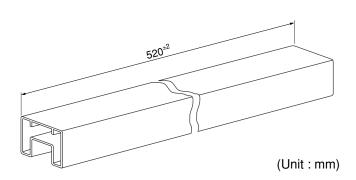
Both ends shall be closed by tabbed and tabless stoppers.

The product shall be arranged in the sleeve with its anode mark on the tabless stopper side.

MAX. 20 sleeves in one case.

Sleeve outline dimensions







● Tape and Reel package

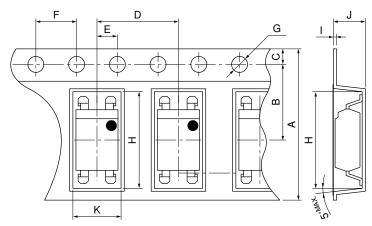
Package materials

Carrier tape: PS

Cover tape: PET (three layer system)

Reel: PS

Carrier tape structure and Dimensions

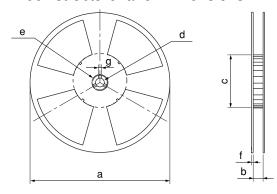


Dimensions List

(Unit: mm)

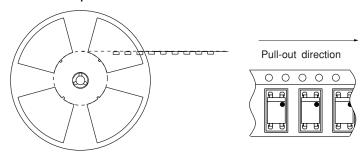
A	В	С	D	Е	F	G
16.0±0.3	7.5 ^{±0.1}	1.75 ^{±0.1}	$8.0^{\pm0.1}$	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 8.1
Н	I	J	K			
10.4 ^{±0.1}	$0.4^{\pm0.05}$	4.2 ^{±0.1}	5.1 ^{±0.1}			

Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	С	d	
330	17.5 ^{±1.5}	100±1.0	13 ^{±0.5}	
e	f	g		
23±1.0	2.0±0.5	2.0±0.5		

Direction of product insertion



[Packing: 2 000pcs/reel]



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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
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 - --- Audio visual equipment
 - --- Consumer electronics
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- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
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